IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: FRANK W. BRICE, JR., ET AL

FOR: MEMORY MAPPED INPUT/OUTPUT VIRTUALIZATION

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR & 1.56, 1.97 AND 1.98

The Commissioner of Patents and Trademarks P.O. Box 1450 Alexandria, VA 22313-1450

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In compliance with the duty to disclose, applicants submit herewith a copy of each publication, excluding U.S. Patents and U.S. published patent applications, of which those designated by 37 CFR § 1.56 are aware; the publications are listed on attached form PTO-1449. The present Information Disclosure Statement shall not be construed as a representation that a search has been made or that no other art than that identified on the attached form PTO-1449 exists.

Consideration of this Information Disclosure Statement is respectfully requested, since the Examiner may consider the information provided herein material to the patentability of the subject application as defined in 37 CFR § 1.56.

Since this Information Disclosure Statement is being filed within three months of the filing date of the subject application, no fee or certification under 37 CFR  $\S$  1.97 (e) is

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required.

However, in the event the Commissioner of Patents deems that any fee is required under 37 CFR §§ 1.16 and 1.17 in connection with this application, applicants' attorneys

authorize that such fee be charged to Deposit Account No. 09-0463.

Respectfully submitted,

FRANK W. BRICE, JR., ET AL

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## INFORMATION DISCLOSURE CITATION

ATTY DOCKET NO. SERIAL NO. POU920030188US1 NYA

Frank W. Brice, Jr. FILING GROUP

(Use several sheets if necessary)

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*EXAMINER INITIAL		DOCUMENT NUMB	ER DATE	N/ME	CLASS	SUBCLASS	FILING DAT				
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/C.Q/	6	JP9069072A	1997-03-11	Japan w/ English Abstract			125	1			
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/C.Q		Formal Properties of Recursive Virtual Machine Architectures. Gerald Belpaire* New York University and Nal-Ting Hsu* University of Wisconsin. *Department of Computer Science, Courant Institute of Mathematical Science. This work is supported by ERDA under contract E(11-)3077. **Computer Science Separtment and Mathematics. Esbasrach Center. This work is supported in part by the US Army contract DAHCO4-75-C-0024. pages 89-96  11/1975									
/C.Q/		DAISY: Dynamic Compilation for 100% Architectural Compatibility. Kemal Ebcioglu and Erik R. Altman. 1BM Thomas J. Watson Research Center, Yorktown Heights, NY 10598. {kemal,erik}@watson.ibm.com. 1997 ACM 8 0-89791-901-7/97/000653.50 pages 26-37									

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/Camquy Truong/

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DATE CONSIDERED

09/24/2008

## ATTY DOCKET NO. SERIAL NO. POU920030188US1 NYA INFORMATION DISCLOSURE CITATION Frank W. Brice, Jr. (Use several sheets if necessary) FILING NYA NYA U.S. PATENT DOCUMENTS FILING DATE \*EXAMINER DATE NAME CLASS SUBCLASS DOCUMENT NUMBER INITIAL IF APPROPRIATE FOREIGN PATENT DOCUMENTS TRANSLATION CLASS SUBCLASS DOCUMENT NUMBER DATE COUNTRY YES OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER /Camquy Truong/ DATE CONSIDERED 09/24/2008

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